

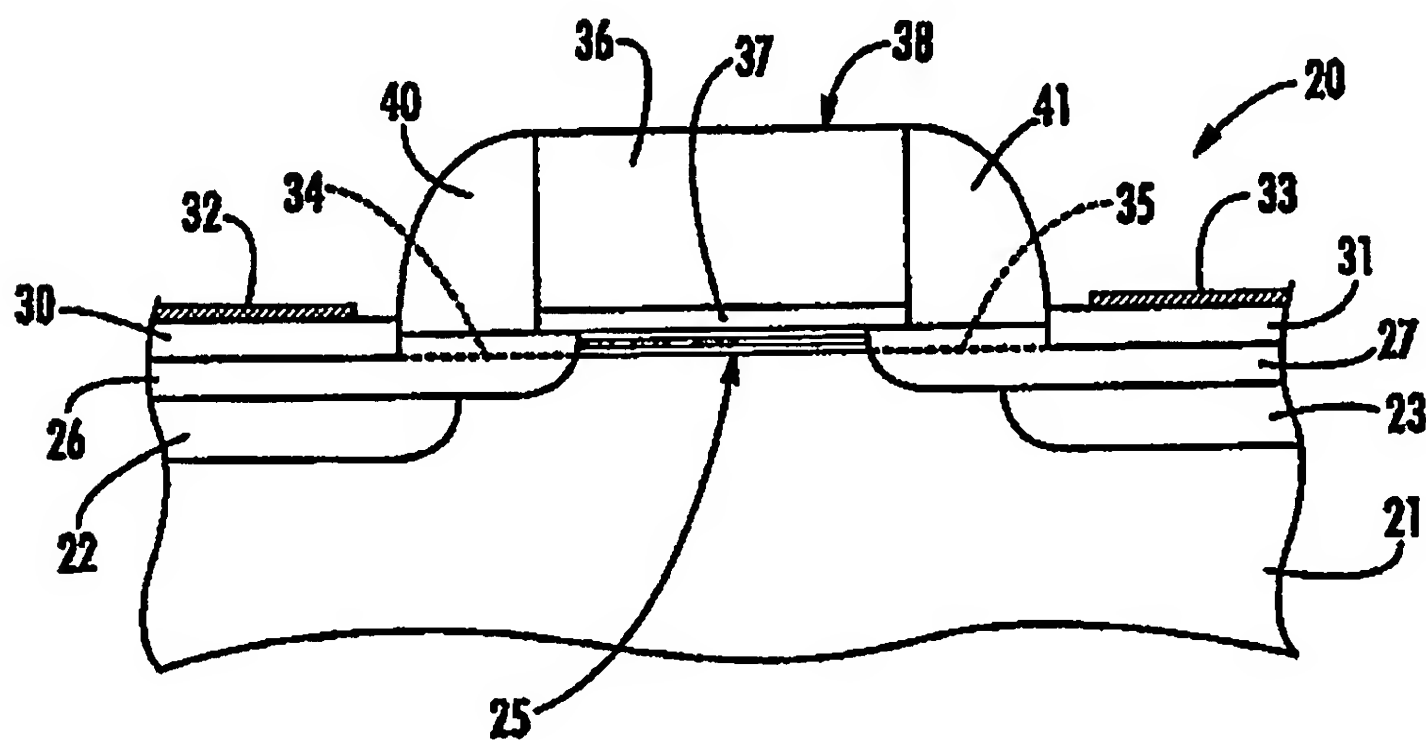
UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,033,437 B2
APPLICATION NO. : 10/717370
DATED : April 25, 2006
INVENTOR(S) : Robert J. Mears et al.

Page 1 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page, Delete: FIG. 1
Drawing Insert: New FIG. 1



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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 2, Line 2 Delete: "in a silicon"
 Insert: --in silicon--

Column 2, Line 3 Delete: "electromuminescence"
 Insert: --electroluminescence--

Column 2, Line 62 Delete: "superlattice and has"
 Insert: --superlattice has--

Column 4, Line 35 Delete:

$$M_{k,j}^I(E_F, T) = \frac{- \sum_{E > E_F} \int_{BZ} (\nabla_k E(k, n))_i (\nabla_k E(k, n))_j \frac{\partial f(E(k, n), E_F, T)}{\partial E} d^3k}{\sum_{E > E_F} \int_{BZ} (1 - f(E(k, n), E_F, T)) d^3k}$$

Insert:

$$M_{k,j}^I(E_F, T) = \frac{- \sum_{E < E_F} \int_{BZ} (\nabla_k E(k, n))_i (\nabla_k E(k, n))_j \frac{\partial f(E(k, n), E_F, T)}{\partial E} d^3k}{\sum_{E < E_F} \int_{BZ} (1 - f(E(k, n), E_F, T)) d^3k}$$

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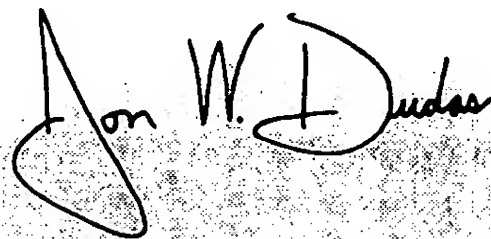
Page 3 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 5, Line 18	Delete: "gate 35" Insert: --gate 38--
Column 5, Line 67	Delete: "gate 35" Insert: --gate 38--
Column 8, Line 4	Delete: "from the both" Insert: --from both--
Column 9, Line 67	Delete: "also formed" Insert: --also be formed--

Signed and Sealed this

Nineteenth Day of December, 2006



JON W. DUDAS
Director of the United States Patent and Trademark Office